Examiner: DINH, SON

GAU: 2824 Classification: 365/203.000

10/826457 Inventor: KUO, CHENG-HSIUNG Status: 30 - DOCKETED NEW CASE - READY FOR EXAMINATION TITUE: MEMORY DEVICE FOR CONTROLLING PROGRAMMING SETUP TIME

Incoming tab report (9 items, not sorted)

ument Type	Applicant Response to Pre-Exam Formalities Notice 09/30/2004 5	h or Declaration filed 09/30/2004 4	wings 09/30/2004 1	nsmittal letter 04/16/2004 2	cdfication 04/16/2004 11	ims 04/16/2004 4	tract 04/16/2004 1	wings 04/16/2004 6	
Document Type	Applicant Response to Pre-Exam	Oath or Declaration filed	Drawings	Transmittal letter	Specification	Claims	Abstract	Drawings	Ooth or Department floor
Doc Code	7 PEFR	7 ОАТН (7 DRW I	7 TRNA	7 SPEC	2 CLM	7 ABST	7 DRW I	
ling String	6	6	6	6	6			@	•

Ref #	Hits	Search Query	DBs Default Operator		Plurals	S Time Stamp	
L1	277	(word adj line\$1) and (setup adj time)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:21	
L2	37	1 and (pre adj charge)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:55	
L3	12	2 and monitor\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:23	
L4	1467	(word adj line\$1) and (pre adj charge)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:23	
L5	155	4 and monitor\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:23	
L6	107	5 and latch\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:23	
L7	80	6 and program\$3	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:24	
L8	80	7 and memory	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:28	
L9	34	8 and threshold	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 10:24	
L10	37	1 and 365/203	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:02	
L11	0	(memory and (word adj line) and (pre adj charge) and program and (monitoring adj circuit))".clm"	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:03	
L12	0	(memory and (word adj line) and (pre adj charge) and program and (monitoring adj circuit)).clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:03	

L13	0	(memory and (word adj line) and (pre adj charge) and (first adj test adj word adj line) and (second adj test adj word adj line) and (monitoring adj circuit)). clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:05
L14	0	((programming adj cycle) and (first adj signal) and (second adj signal) and (monitoring adj circuit) and (source adj lines)).clm.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:06
L15	10263	kuo.inv.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:06
L16	21800	cheng.inv.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:06
L17	2305	hsiung.inv.	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:06
L18	788	15 and 16	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:06
L19	22	17 and 18	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:08
L20	76	(word adj line) and (monitoring adj circuit)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:10
L21	5	20 and (pre adj charge)	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:09
L22	24	20 and precharge	USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2005/09/17 11:10